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24309 7590 03/03/2004

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Pat Slaback	(Depositor's name)
<i>Pat Slaback</i>	(Signature)
May 25, 2004	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/082,517	02/22/2002	Reno L. Sanchez	X-998 US	5981

TITLE OF INVENTION: METHOD AND SYSTEM FOR INSERTING PROBE POINTS IN FPGA-BASED SYSTEM-ON-CHIP (SOC)

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$0	\$1330	06/03/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
DIMYAN, MAGID Y	2825	716-016000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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1. Pablo Meles2. _____
3. _____

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(B) RESIDENCE: (CITY and STATE OR COUNTRY)

XILINX, INC.

San Jose, California 95124

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(Authorized Signature) Pat Slaback (Date) 5/25/04

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COMMISSIONER FOR PATENTS
P. O. Box 1450
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22313-1450

Inventor(s): Reno L. Sanchez et al.



Assignee: XILINX, INC.

Serial No.: 10/082,517

conf.no. 2825

Filed: February 22, 2002

Title: Method and System for Inserting Probe Points in FPGA-Based System-On-Chip

Docket No.: X-998 US

Enclosed: Return Receipt Postcard

Fee Transmittal - Part B

Date: May 25, 2004

VIA FACSIMILE 703-746-4000

Atty/Sec: KK/pbs